In the Claims:

Please amend the claims according to the following listing of claims and substitute it for all prior versions and listings of claims in the application.

1. (original) A digital-to-analog converting circuit of a display, characterized in comprising a plurality of transistors, wherein the ratio of channel-width to channel-length of each transistor is identical.

2. (currently amended) The digital-to-analog converting circuit according to claim 1, further comprising a control device coupled to the transistors to receive a plurality of data bits, so as to control a conducting number among the transistors according to the data bits.

3. (original) The digital-to-analog converting circuit according to claim 1, operative to generate a data current to drive a plurality of pixels of the display.

4. (original) A digital-to-analog converting circuit, applied to a current-type data driver, comprising:

a plurality of transistors; and

a control device coupled to the transistors, the control device being operative to receive a plurality of data bits and to control the number of the conducted transistors among the transistors based on the data bits;

wherein the ratio of the channel-width to channel-length for each of the transistors is the same.

5. (original) The digital-to-analog converting circuit according to claim 4, being operative to generate a data current to drive a plurality of pixels of the display.

6. (new) A digital-to-analog converting circuit, suitable for use in a current-type data driver of a display, the digital-to-analog converting circuit characterized in comprising a plurality of transistors, the ratio of channel-width to channel-length of each of the transistors being the same, wherein a source/drain terminal of each of the transistors is coupled to an operation voltage, the digital-to-analog converting circuit being operative to receive a plurality of data bits and to control the number of the conducted transistors among the transistors based on the data bits to generate an current in accordance with the data bits.

7. (new) A digital-to-analog converting circuit, suitable for use in a current-type data driver of a display, the digital-to-analog converting circuit comprising:

a control device; and

a plurality of transistors, the ratio of channel-width to channel-length of each of the transistors being the same, wherein a source/drain terminal of each of the transistors is coupled to the control device and another source/drain terminal of each of the transistors is coupled to an operation voltage, the control device being operative to receive a plurality of data bits and to

control the number of the conducted transistors among the transistors based on the data bits to generate a current in accordance with the data bits.